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APPLICANTS

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NONE JAT

** FOREIGN APPLICATIONS *****

JAPAN 2000-266971 09/04/2000

YES JAT

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Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials JAT	JAPAN	13	11	1

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TITLE

Semiconductor integrated circuit with suppressed clock skew

FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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